

## Transistors

## 10V Drive Nch MOS FET

## RDN080N25

## ●Structure

Silicon N-channel  
MOS FET

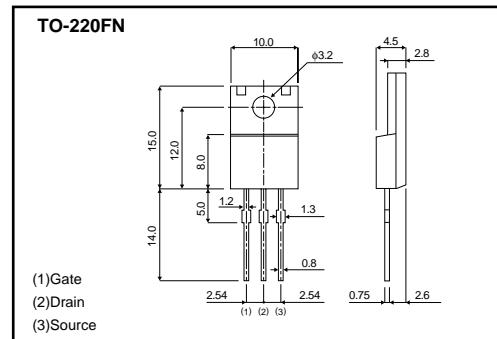
## ●Features

- 1) Low on-resistance.
- 2) Low input capacitance.
- 3) Excellent resistance to damage from static electricity.

## ●Application

Switching

## ●External dimensions (Unit : mm)



## ●Packaging specifications

Type	Package	Bulk
	Code	-
	Basic ordering unit (pieces)	500
RDN080N25		○

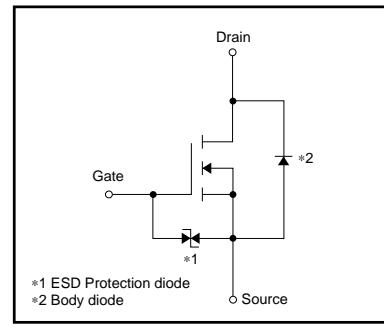
●Absolute maximum ratings ( $T_a=25^\circ\text{C}$ )

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	$V_{DSS}$	250	V
Gate-Source Voltage	$V_{GSS}$	$\pm 30$	V
Drain Current	Continuous $I_D$	8	A
	Pulsed $I_{DP}$	32	A
Reverse Drain Current	Continuous $I_{DR}$	8	A
	Pulsed $I_{DRP}$	32	A
Source Current (Body Diode)	Continuous $I_S$	8	A
	Pulsed $I_{SP}$	32	A
Avalanche Current	$I_{AS}$	8	A
Avalanche Energy	$E_{AS}$	136	mJ
Total Power Dissipation ( $T_c=25^\circ\text{C}$ )	$P_D$	35	W
Channel Temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

\*1  $P_w \leq 10\mu\text{s}$ , Duty cycle  $\leq 1\%$

\*2  $L = 3.4\text{mH}$ ,  $V_{DD}=50\text{V}$ ,  $R_G=25\Omega$ , 1 Pulse,  $T_{ch}=25^\circ\text{C}$

## ●Equivalent circuit



\*1 ESD Protection diode  
\*2 Body diode

\*A protection diode is included between the gate and source terminals to protect the diode against static electricity when the product is in use. Use the protection circuit when the fixed voltages are exceeded.

## ●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to case	$R_{th}(ch-c)$	3.57	$^\circ\text{C}/\text{W}$
Channel to ambient	$R_{th}(ch-a)$	62.5	$^\circ\text{C}/\text{W}$

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## ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±10	µA	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V
Drain-Source Breakdown Voltage	V <sub>(BR) DSS</sub>	250	—	—	V	I <sub>D</sub> =250µA, V <sub>GS</sub> =0V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	25	µA	V <sub>DS</sub> =250V, V <sub>GS</sub> =0V
Gate Threshold Voltage	V <sub>GS (th)</sub>	2.0	—	4.0	V	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA
Static Drain-Source On-State Resistance	R <sub>DSS (on)</sub> *	—	0.38	0.5	Ω	I <sub>D</sub> =4A, V <sub>GS</sub> =10V
Forward Transfer Admittance	Y <sub>fs</sub>   *	1.9	3.1	—	S	V <sub>DS</sub> =10V, I <sub>D</sub> =4.0A
Input Capacitance	C <sub>iss</sub>	—	543	—	pF	V <sub>DS</sub> =10V
Output Capacitance	C <sub>oss</sub>	—	193	—	pF	V <sub>GS</sub> =0V
Reverse Transfer Capacitance	C <sub>rss</sub>	—	64	—	pF	f=1MHz
Turn-On Delay Time	t <sub>d (on)</sub> *	—	13	—	ns	I <sub>D</sub> =4.0A, V <sub>DD</sub> =100V
Rise Time	t <sub>r</sub> *	—	25	—	ns	V <sub>GS</sub> =10V
Turn-Off Delay Time	t <sub>d (off)</sub> *	—	38	—	ns	R <sub>L</sub> =25Ω
Fall Time	t <sub>f</sub> *	—	26	—	ns	R <sub>G</sub> =10Ω
Total Gate Charge	Q <sub>g</sub> *	—	15.0	30.0	nC	V <sub>DD</sub> =125V
Gate-Source Charge	Q <sub>gs</sub> *	—	5.0	—	nC	V <sub>GS</sub> =10V
Gate-Drain Charge	Q <sub>gd</sub> *	—	5.1	—	nC	I <sub>D</sub> =8A

\* Pulsed

## ●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V <sub>SD</sub> *	—	—	2.0	V	I <sub>S</sub> =4A, V <sub>GS</sub> =0V
Reverse recovery time	t <sub>rr</sub> *	—	151	—	ns	I <sub>DR</sub> =8A, V <sub>GS</sub> =0V
Reverse recovery charge	Q <sub>rr</sub> *	—	0.63	—	µC	di/dt= 100A / µs

\* Pulsed

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## ●Electrical characteristic curves

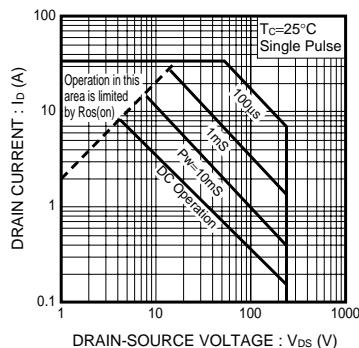


Fig.1 Maximum Safe Operating Area

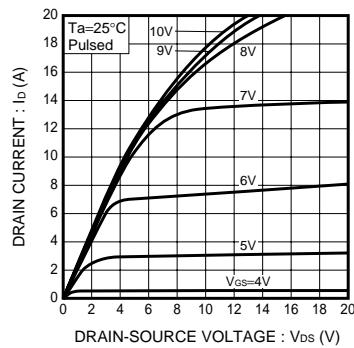


Fig.2 Typical Output Characteristics

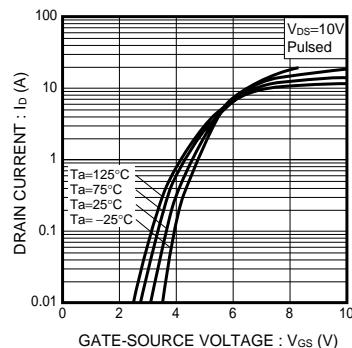


Fig.3 Typical Transfer Characteristics

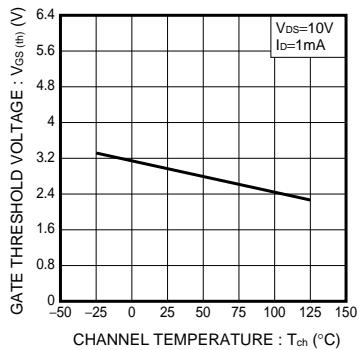


Fig.4 Gate Threshold Voltage vs. Channel Temperature

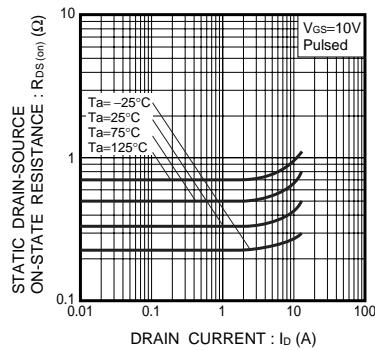


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

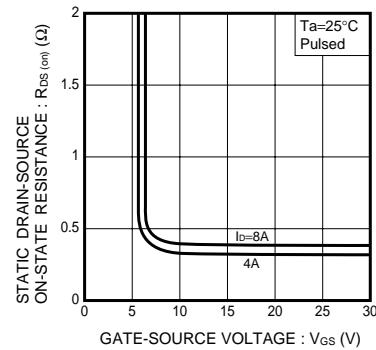


Fig.6 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

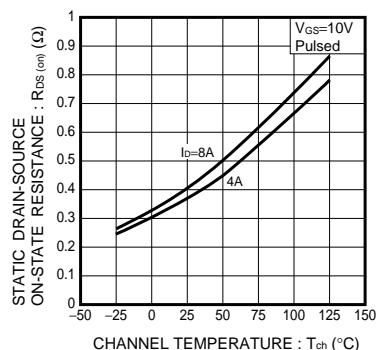


Fig.7 Static Drain-Source On-State Resistance vs. Channel Temperature

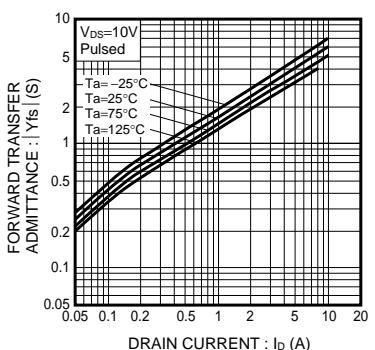


Fig.8 Forward Transfer Admittance vs. Drain Current

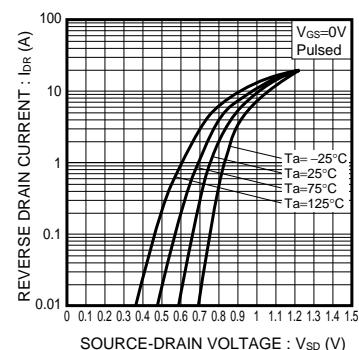


Fig.9 Reverse Drain Current vs. Source-Drain Voltage

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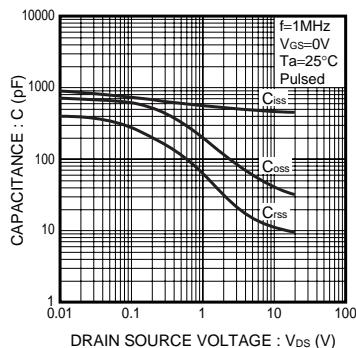
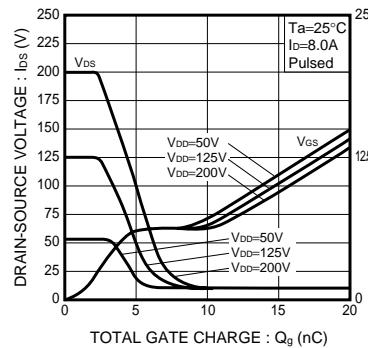
Fig.10 Typical Capacitance vs.  
Drain-Source Voltage

Fig.11 Dynamic Input Characteristics

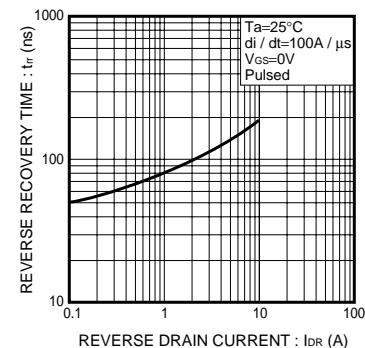
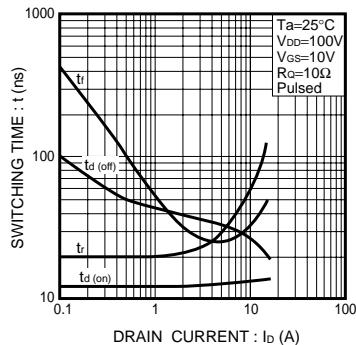
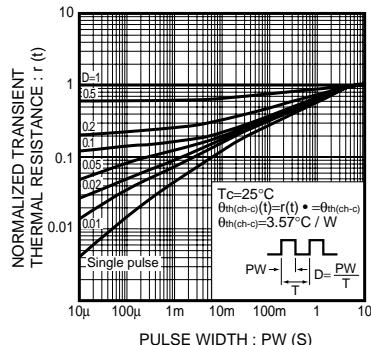
Fig.12 Reverse Recovery Time  
vs. Reverse Drain Current

Fig.13 Switching Characteristics

Fig.14 Normalized Transient  
Thermal Resistance vs.  
Pulse Width

## Appendix

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